// initialize

**fprintf** (pFile,"%dn\t0\n",0,initialVoltage);

**fprintf** (pFile,"%dn\t0\n",DELAY,initialVoltage);

// loop for every sample

**for** (sample = 0 ; sample < NUMBER\_OF\_SAMPLES ; sample++)

{

// Compute a sample

logicLevel = **rand**() % 2;

// print sample

**if** (logicLevel == 0)

{

**fprintf** (pFile,"%fn\t%5.2f\n",(sample\*symbolTime)+DELAY+transitionTime,voltageLow);

**fprintf** (pFile,"%fn\t%5.2f\n",((sample+1)\*symbolTime)+DELAY-transitionTime,voltageLow);

**printf** ("%fn\t%5.2f\n",(sample\*symbolTime)+DELAY,voltageLow);

}

**else**

{

**fprintf** (pFile,"%fn\t%5.2f\n",(sample\*symbolTime)+DELAY+transitionTime,voltageHigh);

**fprintf** (pFile,"%fn\t%5.2f\n",((sample+1)\*symbolTime)+DELAY-transitionTime,voltageHigh);

**printf** ("%fn\t%5.2f\n",(sample\*symbolTime)+DELAY,voltageHigh);

}

}